

#### Is Now Part of



# ON Semiconductor®

# To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



January 2009

# **2SC5200/FJL4315 NPN Epitaxial Silicon Transistor**

#### **Applications**

- · High-Fidelity Audio Output Amplifier
- General Purpose Power Amplifier

#### **Features**

- · High Power Dissipation: 150watts.
- High Frequency: 30MHz.
- High Voltage : V<sub>CEO</sub>=250V
- · Wide S.O.A for reliable operation.
- · Excellent Gain Linearity for low THD.
- Complement to 2SA1943/FJL4215.
- Thermal and electrical Spice models are available.
- · Same transistor is also available in:
  - -- TO3P package, 2SC5242/FJA4313: 130 watts
  - -- TO220 package, FJP5200 : 80 watts
  - -- TO220F package, FJPF5200 : 50 watts



1.Base 2.Collector 3.Emitter

#### Absolute Maximum Ratings\* Ta = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
BV <sub>CBO</sub>	Collector-Base Voltage	250	V	
BV <sub>CEO</sub>	Collector-Emitter Voltage	250	V	
BV <sub>EBO</sub>	Emitter-Base Voltage	5	V	
I <sub>C</sub>	Collector Current(DC)	17	Α	
I <sub>B</sub>	Base Current	1.5	А	
P <sub>D</sub>	Total Device Dissipation(T <sub>C</sub> =25°C) Derate above 25°C	150 1.04	W W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Junction and Storage Temperature	- 50 ~ +150	°C	

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### Thermal Characteristics\* T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Max.	Units	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.83	°C/W	

<sup>\*</sup> Device mounted on minimum pad size

#### **h**<sub>FE</sub> Classification

Classification	R	0
h <sub>FE1</sub>	55 ~ 110	80 ~ 160

## $\textbf{Electrical Characteristics*} \ \, \textbf{T}_{a}\text{=-}25^{\circ}\text{C unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C=5$ mA, $I_E=0$	250			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =10mA, R <sub>BE</sub> =∞	250			<b>V</b>
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E$ =5mA, $I_C$ =0	5			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> =230V, I <sub>E</sub> =0			5.0	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB}$ =5V, $I_C$ =0			5.0	μΑ
h <sub>FE1</sub>	DC Current Gain	$V_{CE}$ =5V, $I_{C}$ =1A	55		160	
h <sub>FE2</sub>	DC Current Gain	$V_{CE}$ =5V, $I_{C}$ =7A	35	60		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =8A, I <sub>B</sub> =0.8A		0.4	3.0	<b>V</b>
V <sub>BE</sub> (on)	Base-Emitter On Voltage	$V_{CE}$ =5V, $I_{C}$ =7A		1.0	1.5	٧
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> =5V, I <sub>C</sub> =1A		30		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =10V, f=1MHz		200		pF

<sup>\*</sup> Pulse Test: Pulse Width=20 $\mu$ s, Duty Cycle≤2%

## **Ordering Information**

Part Number	Marking	Package	Packing Method	Remarks
2SC5200RTU	C5200R	TO-264	TUBE	hFE1 R grade
2SC5200OTU	C5200O	TO-264	TUBE	hFE1 O grade
FJL4315RTU	J4315R	TO-264	TUBE	hFE1 R grade
FJL4315OTU	J4315O	TO-264	TUBE	hFE1 O grade

# **Typical Characteristics**

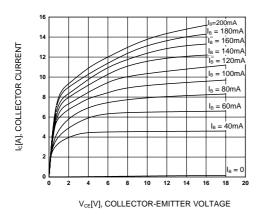


Figure 1. Static Characteristic

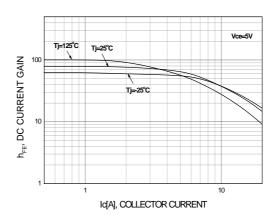


Figure 2. DC current Gain ( R grade )

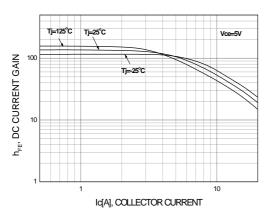


Figure 3. DC current Gain ( O grade )

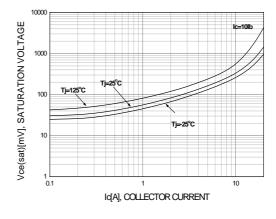


Figure 4. Collector-Emitter Saturation Voltage

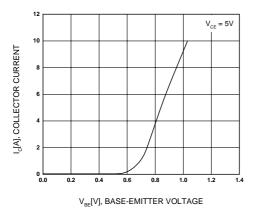


Figure 5. Base-Emitter On Voltage

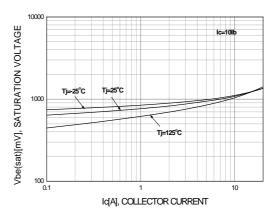


Figure 6. Base-Emitter Saturation Voltage

# **Typical Characteristics**

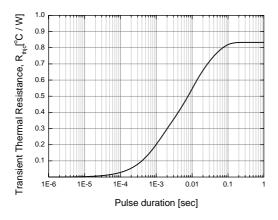


Figure 7. Power Derating

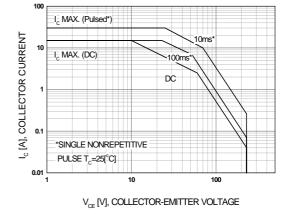


Figure 8. Safe Operating Area

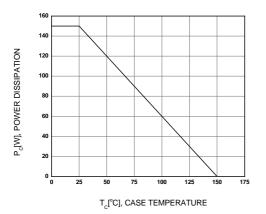
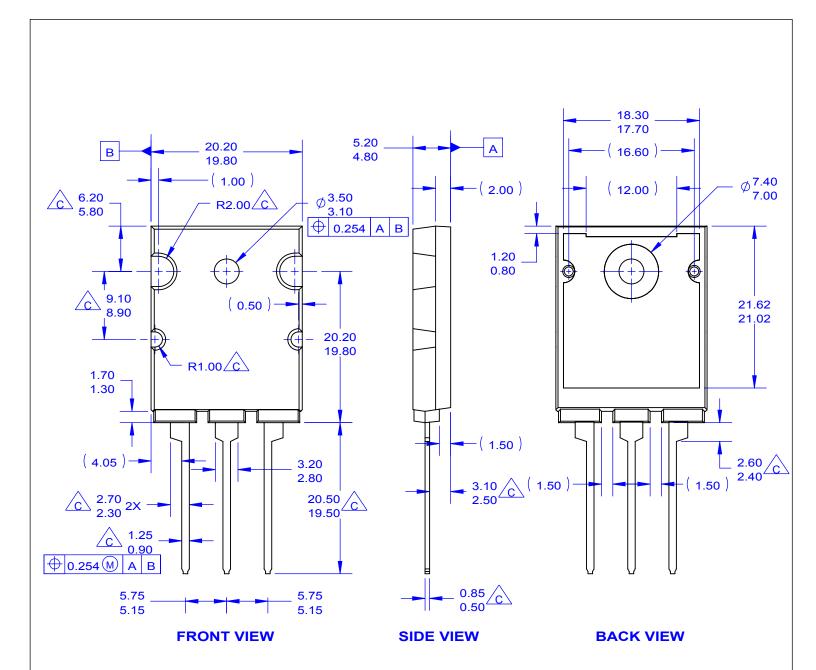
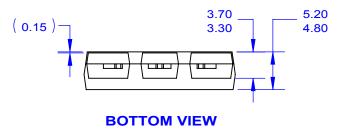


Figure 9. Power Derating







#### NOTES:

- A. PACKAGE REFERENCE: JEDEC TO264 VARIATION AA.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C OUT OF JEDEC STANDARD VALUE.
- D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.
- E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- F. THIS PACKAGE IS INTENDED ONLY FOR "FS PKG CODE AR"
- G. DRAWING FILE NAME: TO264A03REV2

ON Semiconductor and III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages.

Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative